Z86017/Z16017 REFERENCE MANUAL

PREFACE

Thank you for your interest in Zilog's PCMCIA interface solution.
This Reference Manual describes the programming and operation
of the Z86017 and Z16017 PCMCIA adapter chips.

This Reference Manual is organized in the following way:

1 General Overview
Chapter 1 is an introductory section that provides an overview
of the architecture of the device.

2 Addressing Mode
Chapter 2 describes the addressing modes supported by the
Z86017/Z16017 architecture to ensure PCMCIA compatibility.

3 Internal Registers Description
Chapter 3 describes the serial interface modes.

4 Configuration Registers Description
Chapter 4 describes the functions of the Z86017/Z16017
internal registers.

A Appendix A
The Z86017/Z16017 multifunction pins are illustrated in this appendix.

B Appendix B
Absolute Maximum Ratings, DC Electrical Characteristics, and Timing
Specifications related to the Z86017/Z16017 are provided in this
appendix.

C Appendix C
Various Z86017/Z16017 timing diagrams are illustrated in this appendix.

D Appendix D
Part Numbers and ordering information is provided in this appendix.

E Appendix E
A description of the Z8601700ZCO PCMCIA Interface Development Kit is
provided in this appendix.

P R E L I M I N A R Y